



NV4KL

LINE SCAN CMOS IMAGE SENSOR

CHIEF APPLICATION

The NV4KL is a linear array CMOS image sensor chip with an effective pixel array of 4X4096. The chip adopts a 7um global pixel structure to support DCG mode and supports Binning expansion to 14um pixels. The chip uses LVDS output, SPI configuration interface, ADC12bit wide sampling, up to 40K line frequency.

Specification

Item	Parameters
Active Array Size	4(H)*4096(V)
Pixel Size	7.0um x 7.0um
Lens Size	2 inch
Operating Junction Temperature	-40~85°C
Output Format	RAW12
Sensitivity	33.5V/lux.s
Full Well Capacity	10K e ⁻
Dynamic Range	74dB
SNR Max	39dB
Line Rate	40KHz
Chroma	Mono/RGB/RGB_NIR



Application (Reference)

Application Scenarios

- Color Sorter
- Industrial Testing field
- Checking Of Printed stock

Product Feature

- HDR
- On-chip Gain control
- BSI Process
- High Speed data transmission